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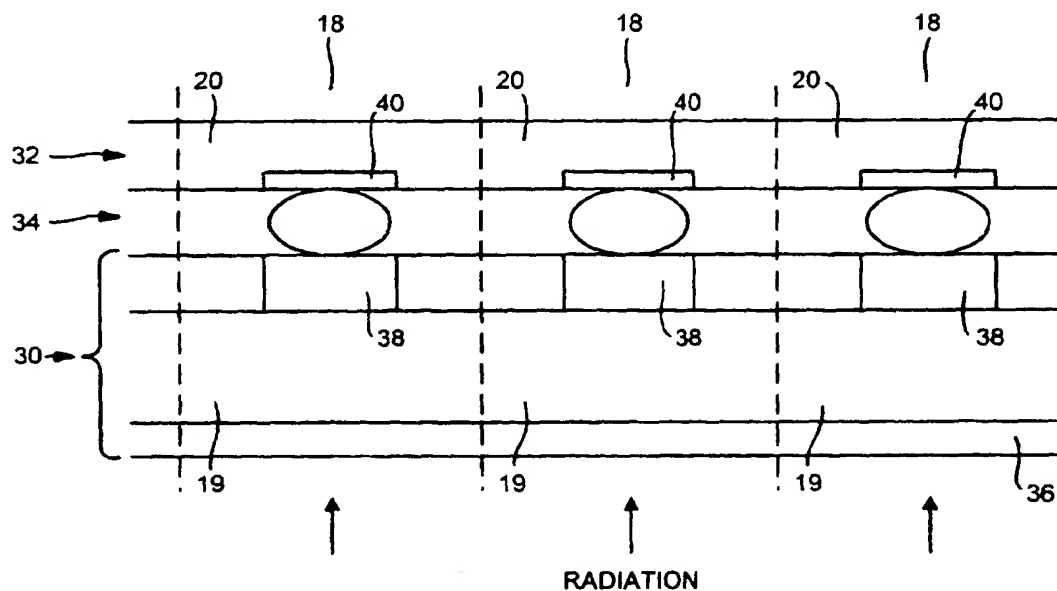
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(58) Field of Search

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KECX KPAC KRG  
INT CL<sup>6</sup> G01T 1/29, H01L 21/60 31/00 31/02 31/0224  
31/115 31/117 31/118 31/119  
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## (54) Bump-bonded semiconductor imaging device

(57) A semiconductor imaging device, for use for example in medical diagnosis and, non-destructive testing, includes a radiation detector semiconductor substrate 30 and a readout substrate 32 connected to the detector substrate by means of low temperature solder bumps 34, the bumps correspond to cells of the two substrates. A low temperature solder should have a melting point under about 180°C. Good examples of such low temperature solder are provided by bismuth based alloys, for instance the eutectic (52wt-%Bi, 32wt-%Pb, 16wt-%Sn) alloy which has a melting point under 100°C, enabling its use with CdZnTe and CdTe detector substances.



RADIATION

FIG. 2

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At least one drawing originally filed was informal and the print reproduced here is taken from a later filed formal copy.

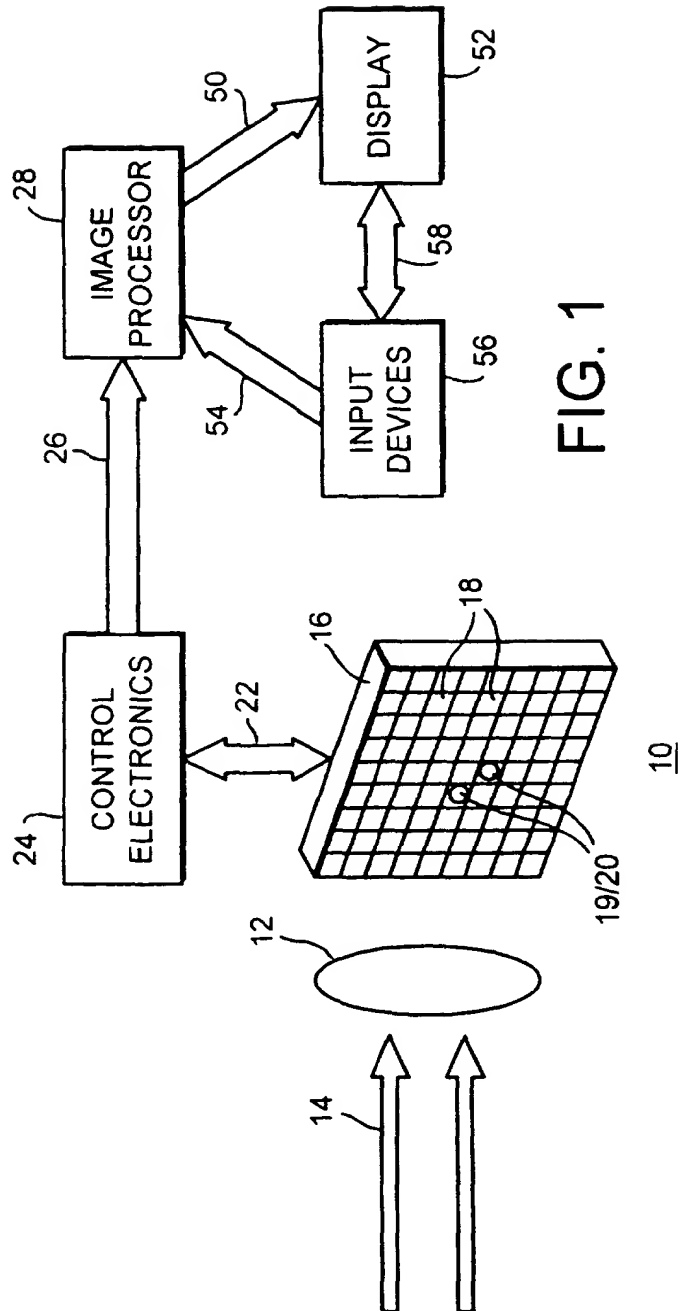
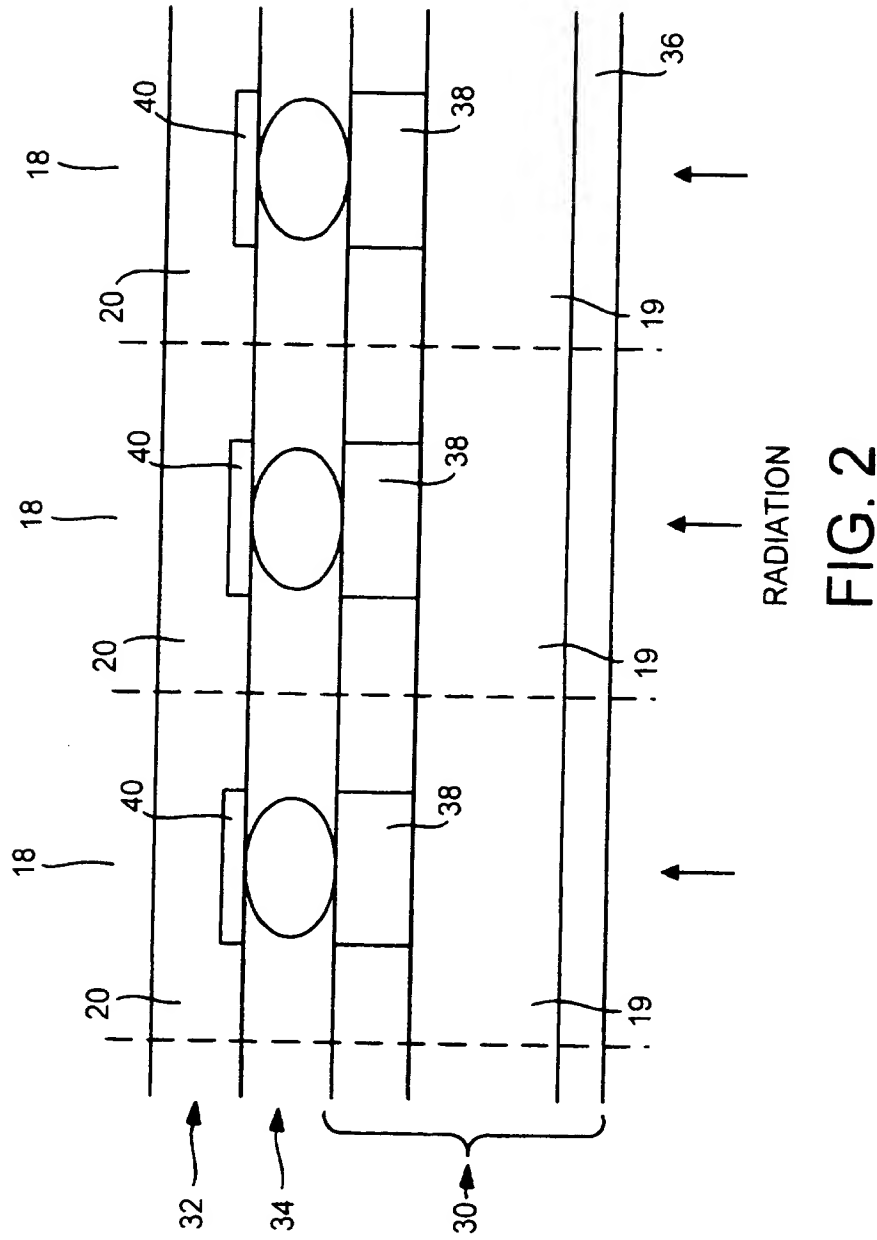
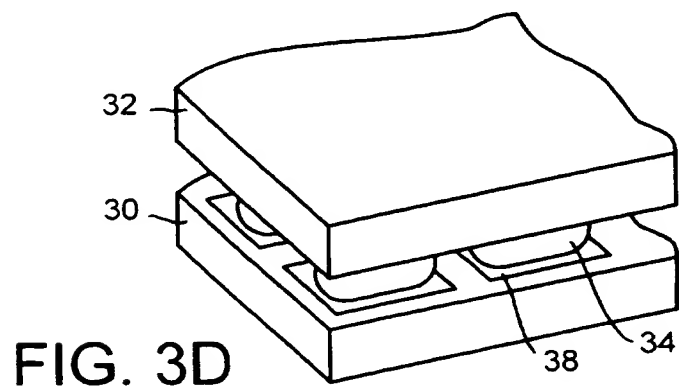
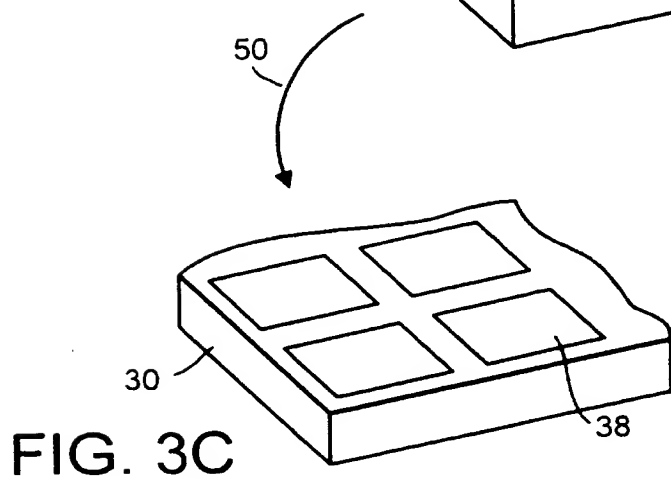
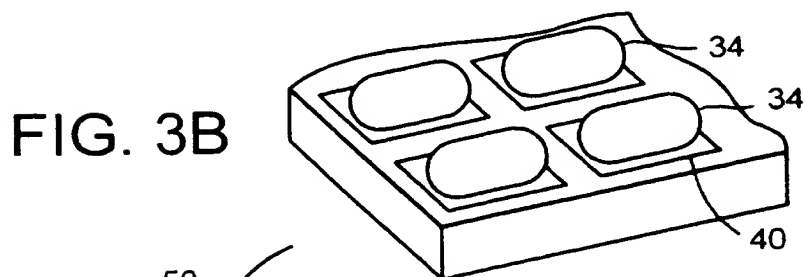
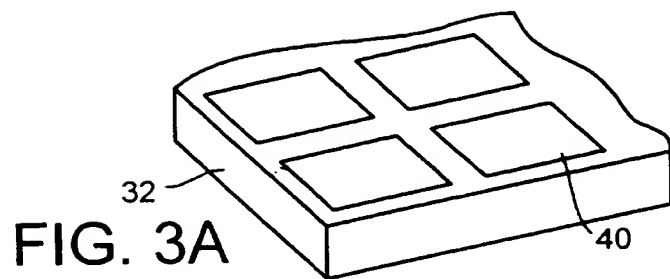


FIG. 1





BUMP-BONDED SEMICONDUCTOR IMAGING DEVICE

The invention relates to an imaging device comprising a detector substrate including a plurality of detector cells bump-bonded to a readout substrate including a corresponding plurality of readout cells and to a method of manufacturing such an  
5 imaging device.

Examples of semiconductors used for such devices are: CdZnTe, Si, CdTe, HgI<sub>2</sub>, InSb, GaAs, Ge, TiBr, PbI<sub>2</sub>.

A detector substrate may comprise a plurality of detector cells (e.g., pixel  
10 cells) defined by metal contacts on one side of the detector. The readout substrate can comprise a corresponding plurality of readout circuits or charge coupled device (CCD) cells. The readout substrate can be bump-bonded to the detector substrate with individual pixel cells being connected corresponding readout circuits or CCD cells by respective conductive bumps.

15 Imaging devices of this type can be used for medical applications involving the exposure of a patient to ionising radiation. Such applications require high radiation absorption characteristics for the detector substrate of the imaging device. Such high radiation absorption characteristics can be provided by materials using high Z elements such as CdZnTe or CdTe. Furthermore, various medical applications  
20 require high spatial resolution. For example, mammography requires the ability to observe microcalcifications which can be under 100 microns or even under 50 microns in size. The stringent requirements imposed on imaging devices require the use of small resolution elements (pixel cells), with a large arrays of such cells being needed to generate an image of a useful size.

25 A most important step in the fabrication of such imaging devices is the bonding of the semiconductor substrate to the readout substrate, or more precisely, the bonding of detector cells to corresponding readout cells in a one-to-one correspondence.

A semiconductor pixel imaging device is disclosed in the Applicant's  
30 International patent application WO95/33332. As mentioned in the previous paragraph, a crucial aspect of this technology is the bonding of the semiconductor substrate to the readout substrate.

Typically, prior art hybrid imaging devices such as those described in US-A-5,245,191, EP-A-0 171 135, and EP-A-0 577 187 employ indium bumps for bump-bonding a detector substrate to a readout substrate.

Indium bumps are grown on the detector metal contacts (defining the cells) and on the readout cells using evaporation. Subsequently, the two different parts are brought together, aligned, and the corresponding bumps are merged. This is also termed flip-chip joining. This cold welding technique is achieved by heating the substrates at 70-120°C and applying mechanical pressure. For detectors comprising heat sensitive materials such as cadmium zinc telluride (CdZnTe) and cadmium telluride (CdTe) the use of indium bumps is advantageous in that the process can be carried out at a low temperature. The temperatures needed for indium bump-bonding, typically 70-120°C, fall within an acceptable range for materials such as CdZnTe and CdTe.

However, during the development of imaging devices using indium bump-bonding, non-uniform detector response has been observed near the detector edges. A plausible explanation is that indium is escaping to the detector edges thus creating undesirable contact between edge metal contacts (edge pixels) and the detector edge.

The present invention seeks to mitigate the problems of the prior art.

In accordance with a first aspect of the invention, there is provided an imaging device for imaging radiation, the imaging device comprising a semiconductor substrate including an array of detector cells generating charge in response to incident radiation and a corresponding readout semiconductor substrate including an array of readout cells, each readout cell being connected to a corresponding detector cell by means of low temperature solder bumps.

An imaging device according to the invention provides improved accuracy and uniformity as a result of the bonding process employed. In particular, the method is self-aligning in that, during heating of the structure, surface tension of the melting bump forces the detector and readout substrates to align with one another.

Although the use of solder for joining circuits together is well known in the electronics arts, the normal type of solder, which is typically formed from 60 weight percent of tin (Sn) and 40 weight percent of lead (Pb), requires the use of temperatures of 183°C or more. Such temperatures, even if applied for only a short

time, damage sensitive detector substrates made of materials such as CdZnTe and CdTe.

Surprisingly, through the use of low temperature solder in accordance with the invention, the disadvantages of indium bump bonding can be avoided without causing damage to the detector substrate, even if it is made of CdZnTe or CdTe, which would be the case were conventional solder to be used.

Moreover, the use of low temperature solder avoids the need to form bumps on both the detector and readout substrates, which provides for economies of manufacture as well as improved performance and reliability. This avoids a further disadvantage of the prior art, which requires the application of indium bumps to both substrates.

Preferably the solder bumps comprise solder having a melting point under 180°C, more preferably below 100°C.

Preferably, the solder comprises an alloy of bismuth (Bi), lead (Pb) and tin (Sn).

A particularly preferred alloy which gives a low melting point of the order of 90°C, comprises approximately 52 weight percent of Bi, approximately 32 weight percent of Pb and approximately 16 weight percent Sn.

As mentioned above, preferred embodiments employ a detector substrate of CdZnTe or CdTe because of the high energy radiation absorption characteristics of those materials. However, it will be appreciated that the invention could be used with other detector substrate materials, even if they are not as temperature sensitive as CdZnTe or CdTe. The readout chip can, for example, be a CMOS chip.

The invention also provides an imaging system comprising at least one imaging device as described above.

An imaging device as described above finds particular application for medical diagnosis and/or for non-destructive testing.

In accordance with another aspect of the invention, there is provided a method of manufacturing an imaging device having an array of image cells for imaging radiation, the imaging device comprising a detector semiconductor substrate including an array of detector cells for generating charge in response to incident radiation and a readout semiconductor substrate including an array of corresponding readout cells,

the method comprising steps of: applying low temperature solder bumps to one of the substrates at positions corresponding to the image cells; aligning respective readout and detector cells to each other; and connecting the detector and readout cells by the application of heat to melt the low temperature solder bumps.

5            Preferably the solder bumps are applied to the readout substrate only, but they could alternatively or additionally be applied to the detector substrate.

          Preferably, to assist in obtaining an accurate alloy composition for the low temperature solder, and thereby to ensure an accurate melting temperature for the low temperature solder, the step of applying low temperature solder bumps comprises, in  
10 successive sub-steps, applying constituent elements of the low temperature solder in required proportions at positions for the solder bumps and then applying heat to reflow the constituent elements to form the solder bumps.

          Exemplary embodiments of the invention will be described hereinafter, with reference to the accompanying drawings in which:

15            Figure 1 is a schematic overview of an imaging system for high energy radiation imaging;

          Figure 2 is a schematic cross sectional diagram of an example of imaging device in accordance with the invention; and

          Figure 3 is a schematic diagram illustrating a method of manufacturing such  
20 an imaging device in accordance with invention.

          Figure 1 is a schematic representation of an example of an imaging system 10 including an embodiment of an imaging device in accordance with the invention.

          This application relates to radiation imaging of an object 12 subjected to radiation 14. The radiation may, for example, be X-ray radiation and the object 12  
25 may, for example, be a part of a human body.

          The imaging device 16 comprises a plurality of pixel cells 18. The imaging device detects directly high energy incident radiation such as X-rays,  $\gamma$ -rays,  $\beta$ -rays or  $\alpha$ -rays. The imaging device is configured on two substrates, one with an array of pixel detectors 19 and one with an array of readout circuits 20, the substrates being  
30 mechanically connected to each other by low temperature solder bumps.

          Control electronics 24 includes processing and control circuitry for controlling the operation of the imaging device, or an array of imaging devices. The control



electronics 24 enables the readout circuits 20 associated with individual pixel cells 18 to be addressed (e.g., scanned) for reading out charge from the readout circuits 20 at the individual pixel cells 18. The charge read out is supplied to Analogue to Digital Converters (ADCs) for digitisation and Data Reduction Processors (DRPs) for processing the digital signal.

The processing which is performed by the DRPs can involve discriminating signals which do not satisfy certain conditions such as a minimum energy level. This is particularly useful when each readout signal corresponds to a single incident radiation event. If the energy corresponding to the measured signal is less than that to be expected for the radiation used, it can be concluded that the reduced charge value stored results from scattering effects. In such a case the measurement can be discarded with a resulting improvement in image resolution.

The control electronics 24 is further interfaced via a path represented schematically by the arrow 26 to an image processor 28. The image processor 28 includes data storage in which it stores digital values representative of the charge values read from each pixel cell along with the position of the pixel cell 18 concerned. The image processor 28 builds up an image for display. It then reads the values stored for the selected pixel positions to cause a representation of the data to be displayed on a display 32 via a path represented schematically by the arrow 30. The data can of course be printed rather than, or in addition to being displayed and can be subjected to further processing operations. Input devices 36, for example a keyboard and/or other typical computer input devices, are provided for controlling the image processor 28 and the display 32 as represented by the arrows 34 and 38.

Figure 2 is a schematic cross section of part of an imaging device 16. In this example, the imaging device 16 comprises an image detector substrate 30 connected to an image circuit substrate 32 by means of solder bumps 34. A pixel detector 19 of each pixel cell 18 is defined on the detector substrate 30 by a continuous electrode 36 which applies a biasing voltage and pixel location electrodes (contact pads) 38 to define a detection zone for the pixel cell 18. Corresponding pixel circuits 20 on the image circuit substrate 32 are defined at locations corresponding to the electrodes 38 (ie to the pixel detectors 19). Electrodes (contact pads) 40 for the pixel circuits 20 are electrically connected to the corresponding electrodes 38 by the solder bumps 34.

In this manner, when charge is generated in a pixel detector 19 in response to incident radiation, this charge is passed via the solder bumps 34 to the corresponding pixel circuit 20.

Thus, each pixel cell 18 of the imaging device 16 is in effect defined on the substrate by electrodes (not shown) which apply a biasing voltage to define a detection zone (i.e., the pixel detector 19) for the pixel cell 18. Corresponding readout circuits on the readout substrate can comprise, for example, active pixel circuits 20 as described in WO95/33332. The pixel detectors 19 are formed with a detection zone such that, when a photon is photo-absorbed in the semiconductor substrate 16 at a pixel cell 18 creating an electric charge or when a charged radiation ionizes the detection zone of the semiconductor substrate 16 at a pixel cell 18, an electric pulse flows from the semiconductor substrate detection zone to the readout circuit 20 for that pixel cell 18 through the solder bump 34 for that pixel cell.

In order to provide efficient charge absorption for X-rays and other high energy radiation typically having energies in excess of 1keV, the use of high absorption semiconductor materials for the detector substrate is desirable, for example, CdZnTe or CdTe. In this case, low temperature processes used during manufacture avoid damaging the temperature sensitive substrate.

Thus, through the use of low temperature soldering (under 180°C) sensitive materials such as CdZnTe or CdTe can be used without impairing the characteristics of the detector substrate.

An example of an imaging device in accordance with the invention, therefore comprises a semiconductor substrate and a readout substrate, the substrates comprising detecting and readout cells respectively, each detecting cell being connected to a corresponding (one-to-one correspondence) readout cell with low temperature solder bumps.

By way of example, monolithic detectors of dimensions 12.2x4.2 mm<sup>2</sup> (41,000 pixels of 35 microns size) and 18.9x9.6 mm<sup>2</sup> (130,000 pixels of 35 microns size) connected to a CMOS chip via low temperature solder bumps may be constructed. However, the actual size of the pixel circuit and the pixel detector will depend on the application for which the imaging device is intended, and the circuit technology used.

Such an imaging device will then exhibit the necessary uniform performance

over a large number of bonded cells thus meeting the criteria (high absorption efficiency, high spatial resolution) for use in medical diagnosis, for example mammography, dental imaging, chest X-rays, conventional X-rays, fluoroscopy, computerised tomography, nuclear medicine and non-destructive testing.

5           Low temperature solder bumps may be as small as 5 microns in diameter but may be larger. A soldering material with low melting point will be a suitable low temperature solder. By a low temperature solder is meant a solder which can be melted at a temperature which will mitigate or prevent damage or deterioration of a temperature sensitive detector substrate such as CdZnTe or CdTe. A low temperature  
10 solder has a melting point of preferably less than 180°C, more preferably less than 120°C and yet more preferably less than 100°C. A suitable example of such a low temperature solder material is a ternary bismuth-lead-tin (BiPbSn) alloy. The melting point of a eutectic (52wt% Bi, 32wt% Pb, 16wt% Sn) alloy is, for example, under 100°C at about 90°C. The percentages of the composition are each approximate. The  
15 alloy may be made solely of the three elements mentioned in approximately the proportions indicated to a total of 100wt%. However, the alloy composition may be varied to optimise wetting, melting point and/or thermal expansion on solidification. For example, the proportions of the component elements may be varied and/or other component elements may be chosen for addition to or substitution for the elements  
20 mentioned.

Figure 3 is a schematic representation of a method of manufacturing an imaging device as described above.

Figure 3A represents a step of providing a readout substrate 32 with an array of contact pads 40 for connections to corresponding contact pads 38 on a detector  
25 substrate 30 (Figure 3C).

Figure 3A represents the provision of solder bumps 34 on the contact pads 40. The solder bumps can be formed, for example, by vacuum evaporation or electroplating for depositing the metal alloy solder material on respective contact pads. A metal or photoresist mask may be used. To attain an accurate alloy  
30 composition, each constituent metal may be deposited separately but then, prior to joining, the structure is subjected to a process step in which the bumps are reflowed, (subjected to a temperature higher than the alloy's melting point) thus homogenising

the bump composition at each contact pad position. It is not necessary to exceed significantly the melting point of the alloy, in order to reflow the layered "sandwich" structure.

5 In a preferred embodiment of the invention, the bump is deposited on the readout chip side only as shown in Figure 3B so as to spare the detector from any harmful deposition and for economy of tasks (avoiding growing bumps on the detector substrate).

Alternatively, task economy could also be achieved by depositing the solder bumps 34 on the detector substrate 30 (Figure 3C) instead, although this would  
10 increase the risk of possible damage to the detector substrate.

As a further alternative, bumps can be grown on both the readout substrate 32 and on the detector substrate 30 if a suitable bump volume cannot be attained otherwise.

A solderable (solder wettable) pad can be formed underneath the solder bump.  
15 This pad can be deposited prior to bump deposition using the same mask. It is not necessary to use the same technique for depositing both the bump and the under-bump metallurgy. An additional advantage provided by low temperature solder is that it allows for thinner under-bump metallurgies, as well as providing the choice of using otherwise unusable metals, as the rate at which the under-bump metallurgy dissolves  
20 into the bump is proportional to temperature.

Guard rings, also made of solder, and in addition to their electrical function, may be used around the pixel array hermetically to seal the pixel area solder joints from external atmosphere. Dams and/or shields, for electrical and/or mechanical purposes, may also be constructed.

25 The bumps need not all be of the same size. A small number of relatively large bumps may be used to aid the self-alignment of the main pixel array with a large number of relatively small bumps.

Once the solder bumps are formed, then the readout substrate 34 (Figure 3B) is flip-chip joined to the detector substrate (Figure 3C), as represented by the arrow  
30 50 with the controlled application of heat at a temperature and for a time sufficient to melt the solder bumps, but not sufficient to cause damage to the semiconductor substrates. In this manner joining of the respective contact pads 38, 40 on the

detector substrate 30 and the readout substrate 32, respectively, can be achieved.

Figure 3D represents one corner of the joined hybrid imaging device 16.

Thus there has been described a semiconductor imaging device, for use for example in medical diagnosis and non-destructive testing, which includes a radiation  
5 detector semiconductor substrate and a readout substrate connected to the detector by means of low temperature solder bumps. A low temperature solder should have a melting point under about 180°C, preferably less than 150°C, more preferably less than 120°C and yet more preferably less than 100°C. Good examples of such low  
10 temperature solders are provided by bismuth-based alloys, for example the eutectic alloy, which is composed of approximately 52wt% Bi, approximately 32wt% Pb and approximately 16wt% Sn to 100wt% and has a melting point under 100°C.

An example of an imaging device in accordance with the invention can be used in applications such as medical diagnosis, for example for mammography, dental imaging, chest X-rays, fluoroscopy, computerised tomography, nuclear medicine and  
15 so on. An example of an imaging device in accordance with the invention can also be used in applications such as non-destructive testing.

Although particular embodiments of the invention, have been described, it will be appreciated that modifications and or additions thereto can be made within the scope of the invention.

CLAIMS

1. An imaging device for imaging radiation, said imaging device comprising a semiconductor substrate including an array of detector cells for generating charge in response to incident radiation and a corresponding readout semiconductor substrate including an array of readout cells, each readout cell being connected to a corresponding detector cell by means of low temperature solder bumps.
2. An imaging device according to Claim 1, where said solder bumps comprise solder having a melting point under 180°C.
3. An imaging device according to Claim 2, wherein said solder has a melting point under 100°C.
4. An imaging device according to any preceding Claim, wherein said solder comprises a BiPbSn alloy.
5. An imaging device according to Claim 4, wherein said solder comprises an alloy of approximately 52 weight percent of Bi, approximately 32 weight percent of Pb and approximately 16 weight percent Sn.
6. An imaging device according to any one of the preceding Claims, wherein said detector substrate comprises CdZnTe or CdTe.
7. A imaging system comprising at least one imaging device according to any preceding Claim.
8. The use of an imaging device according to any one of Claims 1 to 6 for medical diagnosis.
9. The use of an imaging device according to any one of Claims 1 to 6 for non-destructive testing.

10. A method of manufacturing an imaging device having an array of image cells for imaging radiation, said imaging device comprising a detector semiconductor substrate including an array of detector cells for generating charge in response to incident radiation and a readout semiconductor substrate including an array of  
5 corresponding readout cells, said method comprising steps of:
- applying low temperature solder bumps to one of said substrates at positions corresponding to said image cells;
  - aligning respective readout and detector cells to each other; and
  - connecting said detector and readout cells by the application of heat to melt  
10 said low temperature solder bumps.
11. A method according to Claim 10, wherein said solder bumps are applied to said readout substrate at positions corresponding to said readout cells.
- 15 12. A method according to Claim 10, wherein said solder bumps are applied to said readout substrate at positions corresponding to said readout cells and to said detector substrate at positions corresponding to said detector cells.
13. A method according to any one of Claims 10 to 12, wherein said temperature  
20 is low enough to prevent damage to said detector substrate during said application of said heat.
14. A method according to Claim 13, where said solder bumps comprise solder having a melting point under 180°C.
- 25 15. A method according to Claim 13, wherein said solder has a melting point under 100°C.
16. A method according to any one of Claims 10 to 15, wherein said solder  
30 comprises a BiPbSn alloy.
17. A method according to Claim 16, wherein said solder comprises an alloy of

approximately 52 weight percent of Bi, approximately 32 weight percent of Pb and approximately 16 weight percent Sn.

18. A method according to Claim 16 or Claim 17, wherein said step of applying  
5 low temperature solder bumps comprises, in successive sub-steps, applying constituent elements of said low temperature solder in required proportions at positions for said solder bumps and then applying heat to reflow said constituent elements to form said solder bumps.

10 19. An imaging device substantially as hereinbefore described with reference to the accompanying drawings.

20. An imaging system substantially as hereinbefore described with reference to the accompanying drawings.

15

21. A method of manufacturing an imaging device substantially as hereinbefore described with reference to the accompanying drawings.





The  
Patent  
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Application No: GB 9626972.5  
Claims searched: all

Examiner: Martyn Dixon  
Date of search: 18 August 1997

**Patents Act 1977**  
**Search Report under Section 17**

**Databases searched:**

UK Patent Office collections, including GB, EP, WO & US patent specifications, in:

UK Cl (Ed.O): H1K (KEBA,KECB,KECCX,KECD,KECX,KPAC,KRG)

Int Cl (Ed.6): H01L (21/60,31/00,31/02,31/0224,31/115,31/117,31/118,31/119);  
G01T (1/29)

Other: Online: WPI, JAPIO

**Documents considered to be relevant:**

Category	Identity of document and relevant passage	Relevant to claims
X	WO 93/04384 A (General Imaging) see fig 7 and page 11, lines 1 <i>et seq</i>	1,7,10,11
X	EP 0415541 A (Shimadzu) see col 4, lines 3-10 and col 5, lines 31-53	1,6,7,10,11,13
X,E	JP 090083007 A (Shimadzu) see WPI Abstract Accession No 97-251537/199723	1,7,9,10
X,E	JP 090036410 A (Shimadzu) see WPI Abstract Accession No 97-171828/199716	1,7,10
X	Patent Abstracts of Japan [P-842], Vol 13, No 104 & JP630284485A (Shimadzu)	1,7,10

X Document indicating lack of novelty or inventive step  
Y Document indicating lack of inventive step if combined with one or more other documents of same category.  
& Member of the same patent family

A Document indicating technological background and/or state of the art.  
P Document published on or after the declared priority date but before the filing date of this invention.  
E Patent document published on or after, but with priority date earlier than, the filing date of this application.



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